

FIG. 1

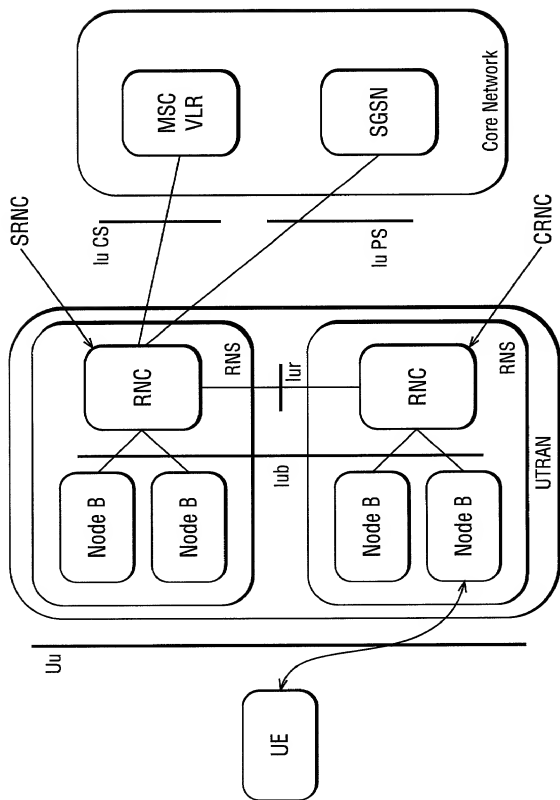


FIG. 2

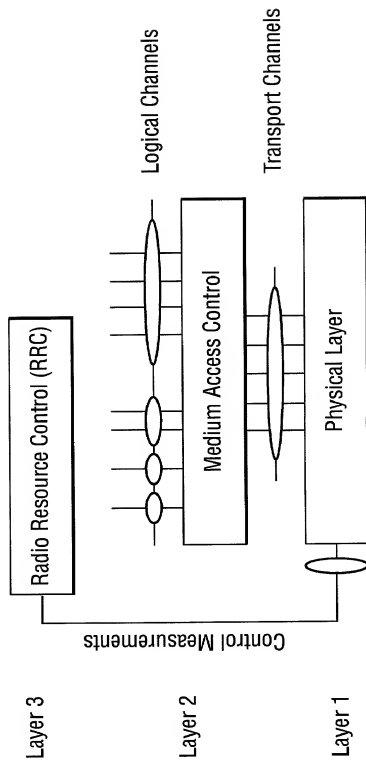


FIG. 3 GP denotes the guard period and CP the chip periods

Chip number (CW)	Length of field in chips	Contents of field
0-975	976	Data symbols
976-1487	512	Midamble
1488-2463	976	Data symbols
2464-2559	96	Guard period

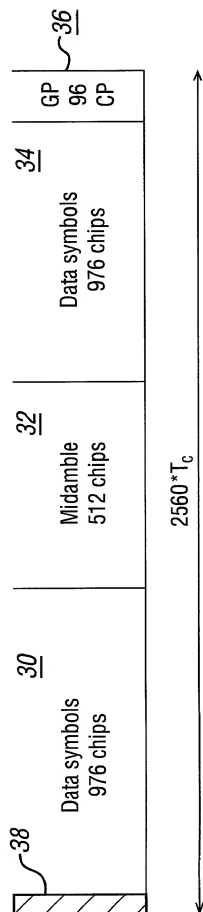


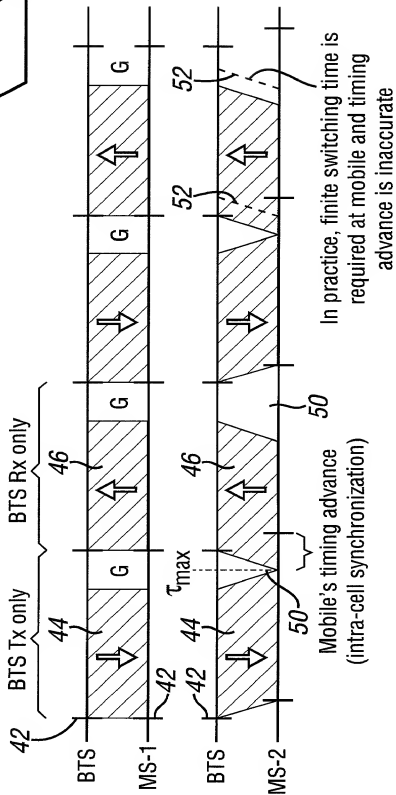
FIG. 4

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Definition	<p>'RX Timing Deviation' is the time difference $TRX_{dev} = TTS - TRX_{path}$ in chips, with TRX_{path}: time of the reception in the Node B of the first significant uplink path to be used in the detection process</p> <p>TTS: time of the beginning of the respective slot according to the Node B internal timing</p>
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FIG. 5

Maximum cell radius is limited by the guard period.



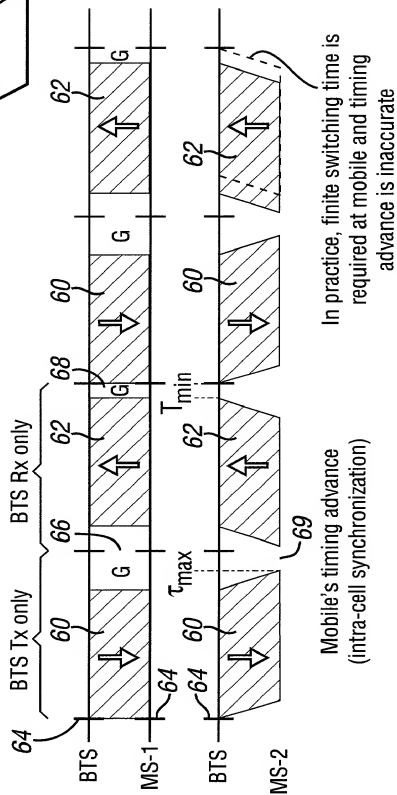
$$\tau_{guard} = 25 \mu s \Rightarrow \tau_{max} = 12.5 \mu s \Rightarrow r_{max} = 3750 m$$

$$\tau_{guard} = 2\tau_{max}$$

In practice, finite switching time is required at mobile and timing advance is inaccurate

FIG. 6

Maximum cell radius is limited by the guard period.



$$T_{guard} = 1/2\tau_{max} \quad T_{guard} = 25 \mu s \Rightarrow \tau_{max} = 50 \mu s \Rightarrow r_{max} = 7500 m$$

FIG. 7

